Compressing 1D Time-Channel Separable Convolutions using Sparse Random Ternary Matrices

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Abstract

We demonstrate that 1x1-convolutions in 1D time-channel separable convolutions may be replaced by constant, sparse random ternary matrices with weights in \{-1, 0, +1\}. Such layers do not perform any multiplications and do not require training. Moreover, the matrices may be generated on the chip during computation and therefore do not require any memory access. With the same parameter budget, we can afford deeper and more expressive models, improving the Pareto frontiers of existing models on several tasks. For command recognition on Google Speech Commands v1, we improve the state-of-the-art accuracy from 97.21\% to 97.41\% at the same network size. Alternatively, we can lower the cost of existing models. For speech recognition on Librispeech, we halve the number of weights to be trained while only sacrificing about 1\% of the floating-point baseline’s word error rate.

1. Introduction

Speech and command recognition tasks tend to have strict low-latency requirements, which may be challenging to meet while using deep neural networks. Specifically, the employment of such networks on edge devices, which are bandwidth, energy, and area constrained, is often too costly in practical applications. Sparsity and quantization techniques are viable solutions, reducing the model size and computational cost while being suited for existing hardware.

With this motivation in mind, we combine sparsity and quantization to make compact speech and command recognition models even more compact. In particular, we leverage recent residual networks \cite{he2016deep} containing time-channel separable convolutions \cite{zagoruyko2016wide, sandler2018mobilenetv2, howard2019searching}. Despite such compact designs, most weights are on the 1x1-convolutions in the residual paths, which are equivalent to fully-connected layers applied to every time step. Hence, we propose to decrease the memory and computational cost by replacing these trained floating-point weights by constant, sparse random ternary matrices.

Our approach enables (i) faster training, since the weights need not be updated, (ii) faster on-chip inference, due to sparse operations with no multiplications, and (iii) higher compression rates, because weights may not be stored but just computed on-the-fly resulting in (iv) reduced memory bandwidth.

At a similar network size, we outperform several floating-point baselines on multiple data sets and tasks. We improve the current state-of-the-art on speech command recognition from 97.21\% to 97.41\% using MatchboxNet \cite{muhammad2021matchboxnet} on Google Speech Commands \cite{graves2014towards}. On AN4 \cite{berner2018an4}, we improve the Pareto efficiency in several floating-point configurations of QuartzNet \cite{chollet2017quartznet}, outperforming deeper and wider baselines at only \(\approx 25\%\) and \(\approx 65\%\) of the network size, respectively. On Librispeech \cite{panayotStateChanged}, when reducing the model size from 19M to 11M parameters we lose only 0.3\% accuracy, achieving 4.30\% word error rate (WER) on dev-clean, as compared to the 3.98\% WER baseline.

2. Related work

After the advances of neural networks in the image domain, they have recently gained popularity in speech recognition. Specifically, recurrent \cite{cho2014learning} and convolutional \cite{chorowski2015attention} neural networks, as well as a combination of both \cite{srullen2017rethinking}, quickly have become common. Encoder-decoder attention networks have also been proposed \cite{ott2017large}, and, following their adoption in the language domain, Transformers have been used for speech tasks as well \cite{li2020transformer, li2021transformers}. However, recurrent networks are less suitable for parallel hardware due to their sequential nature, and attention mechanisms have a high computation and memory footprint.

Recent works show that convolutional networks may outperform recurrent networks even on sequence tasks \cite{fu2021diit} while being better suited to modern parallel hardware. There have been many advances in the design of efficient convolutional models, especially in the computer vision community \cite{howard2019searching, chen2016deep}. Similar network designs have been adapted to speech tasks, replacing 2D-convolutions by 1D-convolutions, \textit{e.g.} in Jasper \cite{dong2019jasper}, a residual convolutional network \cite{huang2017densely}.

Depth-wise separable convolutions have fewer parameters than standard convolutions while improving generalization performance \cite{howard2017mobilenets, chen2016compression}. Directly applying these lightweight convolutions to Jasper results in QuartzNet \cite{chollet2017quartznet}, which reduces the model size by 17\times at similar accuracy. QuartzNet’s residual blocks consist of time-channel separable convolutions, \textit{i.e.} across the time dimensions (temporal-convolutions) and across the feature dimensions (1x1-convolutions), followed by batch normalization (BN) and rectified linear units (ReLU). MatchboxNet \cite{muhammad2021matchboxnet} adopts this design to achieve state-of-the-art results on some speech command recognition data sets. Similar models have also been proposed for speaker recognition and verification \cite{hayashi2020echo} and cross-language learning and domain adaption \cite{zhu2021cross}.

On top of compact architectural design, other compression techniques may be used to further improve model efficiency. While pruning \cite{han2015learning} attempts to find and remove unimportant weights, neurons, or layers, factorization \cite{he2014channel} compresses the weights using a low-dimensional approximation, and quantization \cite{courbariaux2016binaryconnect, rastegari2016xnor} allows one to store and perform operations in low-bit precision reducing the computational cost significantly \cite{agarwal2018image}. Considering ternary weights, \textit{e.g.} \{-1, 0, +1\}, multiplications are reduced to bit-wise, sparse operations in hardware, which are far less costly in terms of energy, speed, and area \cite{chen2015compressing}.

In another line of work, it has been shown that neural networks with random weights, \textit{e.g.} echo state networks \cite{jaeger2001echo} and extreme learning machines \cite{huang2006extreme}, can have surprisingly power-
ful capabilities [28]. These findings have also been shown to translate to automatic speech recognition tasks [29].

In this work, we combine ternary quantization with random weights by replacing weights in 1x1-convolutions with constant, random ternary weights. As these weights neither need to be stored nor updated, a significant reduction in the memory and compute costs is possible for both training and inference.

3. Random ternary matrices

By analyzing the weights of the 1x1-convolutions in the sequence of residual blocks, we observe that they do not expose any obvious visual structure. Specifically, after applying trained ternarized quantization (TTQ [25]), the resulting trained matrices maintain model performance even though they can hardly be distinguished from random ternary matrices (see Figure 1).

As previously mentioned, neural networks with random weights show powerful capabilities [26, 27, 30]. Therefore, we propose to use random ternarized matrices for the 1x1-convolutions in the residual blocks by keeping them constant while training the rest of the network. For each neuron, the ternarized weights simply compute a scaled difference of sums

\[
\text{ReLU} \left( \sum_{i \in I^+} x_i - \sum_{i \in I^-} x_i \right)
\]

where the set \( I^+ \) contains all the indices of the connections with the weight \(+1\) and \( I^- \) contains all the indices of the connections with the weight \(-1\). The union \( I := I^+ \cup I^- \) may be sparse, \( i.e. \) may not contain the indices of all neurons in the previous layer. This scaled sum is a far cheaper operation than the traditional floating-point matrix multiplication.

Similar to echo state networks [26], a sufficient routing capacity is enabled by having trained layers before and after the constant random ternary layers. This matches existing observations [25, 22], where starting quantization too early in the network degrades performance. With this in mind, we only ternarize and do not update the weights in the 1x1-convolutions inside the residual blocks (which possess the majority of the weights). Since the weights of the preceding temporal-convolution and the subsequent batch normalization layers are still updated, a scaling of the inputs and outputs may be learned, respectively.

Implicitly, the tensor product of the aforementioned scaling vectors defines the weights of the ternary matrix, empowering our approach. We note that the ternary matrix in combination with batch normalization is reminiscent of operational amplifiers, computing a difference of sums that is amplified before applying a non-linearity, \( e.g. \) ReLU. Moreover, our algorithm is related to extreme learning machines [27], where a more theoretical foundation can be derived from [31, Algorithm ELM].

3.1. Implementation details

Given a randomly initialized matrix, the random (and constant) ternary matrix may be generated by applying the thresholding procedure presented by Zhu et al. [25]. A parameter \( t \) then serves as a threshold, where all absolute values less than \( t \) are quantized to zero and the remaining values are quantized to \(-1\) and \(+1\) according to their sign. If the matrices are initialized using uniformly distributed samples from \([-1, 1] \), \( t \in [0, 1] \) will be the expected fraction of sparsity of the matrix: \( t = 0 \) yields a dense, binary matrix, \( t = 1 \) yields a zero matrix, and \( t \in [0,1] \) yields a ternary matrix. Listing 1 shows a simple implementation of our approach using PyTorch.

Listing 1: Example implementation given an uniformly distributed weight from \([-1,1] \) and a threshold \( t \in [0,1] \).

```python
# compute signs
pos = (weight.data > 0.).int2()
neg = (weight.data < 0.).int2()

# compute mask
mask = (torch.abs(weight.data) > t).int2()

# make ternary
weight.data = (pos - neg) * mask

# do not update weight during training
weight.requires_grad = False
```

3.2. Implementation variants

Our approach is widely applicable and suited to existing hardware, allowing for a range of efficient implementations. Specifically, the random ternary matrices may be computed on-the-fly on the chip without accessing memory for weights. For example, a pseudo-random number generator may be used to determine the ternary values, where, given an initial state, a specific matrix may be deterministically generated at any time. Alternatively, each ternary value may be determined by a hash function of the matrix entry coordinates, where different matrices may be realized by different hash functions.

Specific, structured sparsity patterns supported by certain accelerators\(^1\) may be applied instead, taking full advantage of hardware support. Further compute acceleration in the ternary layers may be leveraged by custom layer implementations.

4. Results

We conducted experiments using MatchboxNet-NxMxW [3] on Google Speech Commands (GSC) [5] and QuartzNet-NxMxW [2] on AN4 [6] for 200 epochs and Librispeech [7] for 300 epochs. As for the notation, \( N \) stands for the number of residual blocks, \( M \) for the number of depth-wise separable convolutions in the residual blocks, and \( W \) for the number of channels. We refer to the original papers for additional architectural details.

GSCv1 consists of 65k one-second spoken utterances from various speakers. There are 30 command classes chosen for possible IoT or robotics application scenarios, numerical digits, as well as phoneme variety and command similarity. AN4

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\(^1\)https://developer.nvidia.com/blog/exploiting-ampere-structured-sparisty-with-cusparse

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is an alphanumeric data set consisting of around 1k utterances averaging 3 seconds long, describing personal information and control words. Librispeech contains 10k hours of segmented and aligned narrations of public audiobooks and is commonly used to evaluate large-scale speech recognition models.

In our experiments, we compared our simplified networks to the original baselines under identical training settings, i.e. without fine-tuning. We adopted the training setups publicly available at NVIDIA NeMo\(^\text{3}\), so that our results may be easily replicated as well as applied to additional models and tasks.

### 4.1. Speech command recognition (MatchboxNet on GSC)

Figure 2 shows the weight matrices resulting from multiple model size reductions. Figure 3 compares the test accuracy of several MatchboxNet configurations with respect to the number of model parameters. With 200 epochs, our MatchboxNet-3x1x64 baseline achieves 97.09% test accuracy, which is close to the originally reported mean of 97.21% accuracy over five trials [3]. We observe that using random ternary matrices improves the Pareto efficiency of the models, reaching a better trade-off of error rate for model size and cost. Hence, using random ternary weights with and without identity skip links allows us to increase model width and depth efficiently. We trained our models with varied \(t\) values and plot the best models for each configuration. Note that the sparsity levels \(t\) have little influence on the overall performance as discussed later in Section 4.2.1.

Although ternarizing the skip links reduces performance in our experiments, using identity skip links avoids the otherwise quadratic weight matrices and performs close to the baseline for the smaller model configurations. The skip links even may be omitted. Yet deeper models train better with skip links [1]. Interestingly, tests using constant floating-point random matrices did not result in any improvements as compared to our more efficient random ternary matrices.

Table 1 compares our random ternary variants to the floating-point models reported by Majumdar et al. [3]. At 77K parameters, our random ternary MatchboxNet-6x1x64 sets a new state-of-the-art of 97.41% accuracy. Our smaller variants further reduce model size while maintaining high accuracy.

<table>
<thead>
<tr>
<th>Model</th>
<th>FP32 param.</th>
<th>Acc. (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>DenseNet-BC-100 [32]</td>
<td>800 (-10^3)</td>
<td>96.77</td>
</tr>
<tr>
<td>ResNet-15 [33]</td>
<td>238 (-10^3)</td>
<td>95.80</td>
</tr>
<tr>
<td>EdgeSpeechNet-A [34]</td>
<td>107 (-10^3)</td>
<td>96.80</td>
</tr>
<tr>
<td>MatchboxNet-3x1x64 [3] (FP)</td>
<td>77 (-10^3)</td>
<td>97.21</td>
</tr>
<tr>
<td>MatchboxNet-6x1x64 (RT)</td>
<td>77 (-10^3)</td>
<td>97.41</td>
</tr>
<tr>
<td>MatchboxNet-3x2x64 (RT)</td>
<td>64 (-10^3)</td>
<td>97.16</td>
</tr>
<tr>
<td>MatchboxNet-3x1x64 (RT)</td>
<td>60 (-10^3)</td>
<td>97.07</td>
</tr>
</tbody>
</table>

### 4.2. Automatic speech recognition

In Section 4.2.1, we use the fast iterations enabled by using the small AN4 data set to study a range of sparsity levels as well as the Pareto efficiency on multiple QuartzNet configurations. We then extrapolate these findings and evaluate our method against large-scale QuartzNet models on Librispeech (Section 4.2.2).

#### 4.2.1. QuartzNet on AN4

Figure 4 shows that at 90% average sparsity, i.e. \(t = 0.9\), different configurations of QuartzNet perform comparably to the less computationally efficient fully dense models. We find a similar pattern for MatchboxNet. Therefore, we can choose a highly sparse network to minimize memory and compute cost. The performance starts deteriorating with increased sparsity levels, as the 1x1-convolutions eventually become zero matrices.

Figure 5 compares several QuartzNet configurations in
terms of word error rate and network size. Our constant random ternary variants, with and without identity skip links, create a new Pareto frontier improving the accuracy of smaller models.

Figure 4: Word error rate of different QuartzNet models with sparse ternary weights on the AN4 data set for varying sparsity levels. High sparsity levels deliver performance similar to fully connected layers, especially for bigger networks, that are less sensitive to sparsity.

4.2.2. QuartzNet on Librispeech

Using the original QuartzNet-15x5 [2], which has 15 residual blocks, we randomize and ternarize all 1x1-convolutions in the last 6 blocks, reducing the number of trainable parameters by half. Since early layers contain fewer parameters and are more sensitive to quantization [24, 22], compressing later layers presents greater benefits. We trained the skip links and used $t = 0.001$, although sweeping for optimal $t$ values (similar to our previous experiments) would likely improve results.

Table 2 compares the greedy WER of our variant to the bigger models as reported by Kriman et al. [2]. Despite reducing the number of floating-point parameters by half, our variant achieves competitive results. Namely, the WER is only increased by $\approx 1\%$ (test-clean) as compared to the floating-point QuartzNet-15x5 baseline. By also learning the skip links, we manage to reduce the WER by $\approx 0.5\%$ (test-clean) while only learning 2M additional parameters.

We compared our variants to QuartzNet configurations reported by Kriman et al. [2] in Table 3. At identical network sizes, we observe that our random ternary variants either maintain or increase parameter efficiency on dev-clean and dev-other, respectively, especially when also learning the skip links.

Table 2: Word error rate and model size on Librispeech. "FP" denotes floating-point weights and "RT" denotes our random, ternary weights.

<table>
<thead>
<tr>
<th>Model</th>
<th>FP32 param.</th>
<th>Test clean</th>
<th>Dev other</th>
</tr>
</thead>
<tbody>
<tr>
<td>QuartzNet-15x5 [2]</td>
<td>19 $\cdot 10^6$</td>
<td>3.98</td>
<td>11.58</td>
</tr>
<tr>
<td>QuartzNet-10x5 [2]</td>
<td>13 $\cdot 10^6$</td>
<td>4.14</td>
<td>12.33</td>
</tr>
<tr>
<td>QuartzNet (FP, extrapolated)</td>
<td>9 $\cdot 10^6$</td>
<td>4.84</td>
<td>14.20</td>
</tr>
<tr>
<td>QuartzNet-5x5 [2] (FP)</td>
<td>7 $\cdot 10^6$</td>
<td>5.39</td>
<td>15.69</td>
</tr>
<tr>
<td>QuartzNet-15x5 (RT, skip FP)</td>
<td>11 $\cdot 10^6$</td>
<td>4.30</td>
<td>12.87</td>
</tr>
<tr>
<td>QuartzNet-15x5 (RT)</td>
<td>9 $\cdot 10^6$</td>
<td>4.84</td>
<td>13.75</td>
</tr>
</tbody>
</table>

5. Conclusion and future work

Random ternary 1x1-convolutions improve the efficiency of speech residual networks, reducing memory and computational cost significantly during training and inference. Our method is energy efficient and simple to support in hardware: while computation may be realized on-chip without multiplications, memory access may be avoided by computing the weights on-the-fly. Hence, our random ternary layers are a simple, yet effective way to further compact existing network designs.

Due to its similarity to operational amplifiers, our algorithm may be explored in analog hardware in the future. Testing the applicability of our approach to other compact convolutional speech models, e.g. Conformer [13], is worth investigating.

6. Acknowledgements

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7. References


